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INFORMATIONAL CLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)			Applicant Yasuo FUKUDA et al.	
			Filing Date June 8, 2006	Group

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
/G.N.R./		6 1 0 7 1 9 7	08/22/00	SUZUKI			
/G.N.R./		6 2 7 7 6 5 7	08/21/01	NOZAWA et al.			
/G.N.R./		6 2 3 9 0 4 5	05/29/01	TANAKA et al.			

FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
/G.N.R./	9	- 1 9 0 9 7 9	07/22/97	JAPAN			
/G.N.R./	2000	- 9 1 2 4 0	03/31/00	JAPAN			
/G.N.R./	7	- 8 6 2 2 0	03/31/95	JAPAN			X
/G.N.R./	2000	- 3 1 0 7 1	01/28/00	JAPAN			
/G.N.R./	2002	/ 0 9 7 8 6 4	12/05/02	W.I.P.O			

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
/G.N.R./	1	Semiconductor Silicon Crystal Technology, "VLSI Handbook," Academic Press; 1985, pages 125-128, including a partial English translation.

EXAMINER /G. Nagesh Rao/ (01/15/2009)	DATE CONSIDERED 01/15/2009
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*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.